

In the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims

1-5. (Canceled)

6. (Previously presented) A system in which a plurality of companion integrated circuit components collectively implement a logic function embodied in a single, conventional integrated circuit component, comprising:

a host integrated circuit component communicating with other integrated circuit components via a system bus;

a first integrated circuit component comprising logic for interfacing with a first portion of system bus;

a second integrated circuit component comprising logic for interfacing with a second portion of system bus;

a third integrated circuit component not directly coupled with the system bus and comprising logic for communicating with the host integrated circuit via the first and second integrated circuit components, wherein the first integrated circuit component, the second integrated circuit component, and the third integrated circuit component are provided in separate integrated circuit chips; and

at least one additional integrated circuit component not directly coupled with the system bus, and comprising logic for communicating with the host integrated circuit via the first, second, and third integrated circuit components.

7. (Canceled)

8. (Previously presented) The system of claim 6, wherein the third integrated circuit further comprising a functional logic block that performs a conventional functional operation.

9-10. (Canceled)

11. (Previously presented) An integrated circuit component comprising two independent logic portions, each logic portion being capable of being alternatively configured to communicate with a host integrated circuit via a portion of a system bus and a companion integrated circuit and to receive information that is communicated from the companion integrated circuit, which information was communicated to the companion integrated circuit via a portion of a system bus.

12. (Canceled).